

REMARKS/ARGUMENTS

The above-identified patent application has been amended and reconsideration and re-examination are hereby requested.

Claim 1 has been amended to more clearly and distinctly point out the subject matter applicants regard as their invention in view of the rejection under 35 USC 112.

Claims 1-4 stand rejected under 35 USC 103 as being unpatentable over Horowitz et al, in view of Susnow et al,

Claim 1, as now amended points out that the system includes a logic for determining mismatches between the data and the interspersed pattern of special characters in the converted low byte parallel link and the data and the interspersed pattern of special characters in the converted high byte parallel link and for producing a reset signal for the serializer-deserializer when a predetermined plurality of mismatches is determined. Susnow et al, do not determining mismatches between the data and the interspersed pattern of special characters in the converted low byte parallel link and the data and the interspersed pattern of special characters in the converted high byte parallel link. Susnow et al. checks for misalignment between two clock domains; one byte as the byte goes from one clock domain to another clock domain. Susnow et al. does not compare mismatches between the data and the interspersed pattern of special characters in the converted **low byte parallel link** and the data and the interspersed pattern of special characters in the converted **high byte parallel link**.

Claim 3 points out that the system analyzer includes a system for determining whether the data and interspersed pattern of special characters in the converted **low byte parallel link mismatch** the data and the interspersed pattern of special characters in the converted **high byte parallel link**, a determined mismatch indicating the high byte parallel link is not aligned with the low byte parallel link thereby distinguishing over Susnow et al.

Claim 4 points out that the system analyzer includes a system for determining whether the data and pattern of special characters in the parallel **low byte link matches** the data and the pattern of special characters in the parallel **high byte link** thereby distinguishing

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over Susnow et al.

In the event a petition for extension of time is required by this paper and not otherwise provided, such petition is hereby made and authorization is provided herewith to charge deposit account No. 05-0889 for the cost of such extension.

In the event any additional fee is required, please charge such amount to Patent and Trademark Office Deposit Account No. 05-0889.

Respectfully submitted,

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Date

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